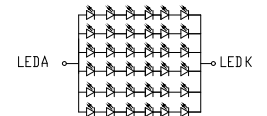


PIN ASSIGNMENT FOR CN1	
PIN	SYMBOL
1	VDD
2	VDD
3	GND
4	GND
5	INO-
6	INO+
7	GND
8	IN1-
9	IN1+
10	GND
11	IN2-
12	IN2+
13	GND
14	CLK-
15	CLK+
16	GND
17	NC
18	NC
19	GND
20	GND

PIN ASSIGNMENT FOR CN2	
PIN	SYMBOL
1	LEDA (RED WIRE)
2	LEDK (WHITE WIRE)


NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VDD=3.3V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: TBD
5. OPERATING TEMP.: -20°C ~ 70°C
6. STORAGE TEMP.: -30°C ~ 80°C
7. LED BACKLIGHT: WHITE LED, IF=120MA, VF=16.8V~19.2V
8. SURFACE LUMINANCE: 400CD/M² (TYP.)
9. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
10. CN1 CONNECTOR: HRS DF19K-20P-1H OR EQUIVALENT
CN2 CONNECTOR: JST BHSR-02VS-1 OR EQUIVALENT
CN2 RECOMMEND MATCHING CONNECTOR: JST SM02B-BHSS-1-TB OR EQUIVALENT
11. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
12. GENERAL TOLERANCE: ±0.5MM
13. ROHS COMPLIANT

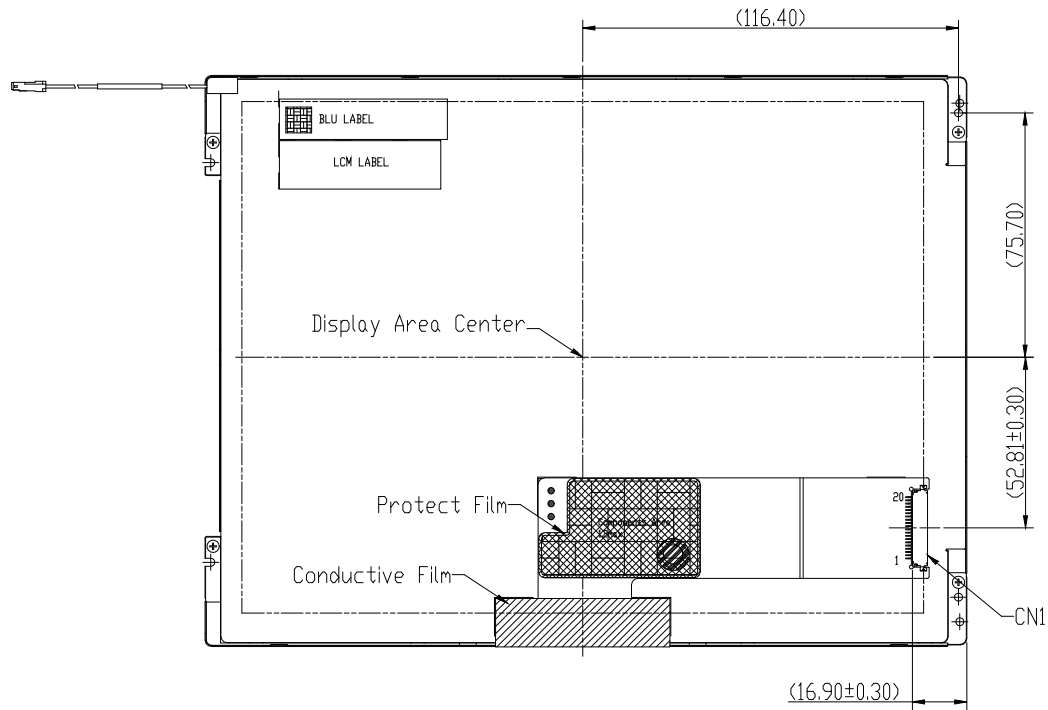


Backlight Circuit Diagram
6S-6P; If=20mA/LED

 MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA	2023.10.16		MODULE P/N:
CHECKED BY:			SCALE : 1/1	M1040ABT-1
APPROVED BY:			UNIT: mm	DESCRIPTION:
VER.	01	FIRST ISSUE	SHEET NO.: 1/2	TFT MODULE

01	FIRST ISSUE	RITA	2023.10.16
VER.	REVISED DESCRIPTION	REVISOR	DATE



MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA	2023.10.16		MODULE P/N:
CHECKED BY:			SCALE : 1/1	M1040ABT-1
APPROVED BY:			UNIT: mm	DESCRIPTION:
VERSION NO:	01		SHEET NO.: 2/2	TFT MODULE

01	FIRST ISSUE	RITA	2023.10.16
VER.	REVISED DESCRIPTION	REVISER	DATE